

_____ , _____ , _____

This lab exercise is to be performed on an individual basis. Any signs of academic dishonesty will result in a grade of 0 for all involved.

You may use your previous labs for reference. You may be penalized for asking questions to which you should know the answer. Good Luck.

Procedure:

- 1.) Design, build and test a circuit which will produce the given truth table. You can use any of the chips available in the lab.

X	Y	Z	$f(X, Y, Z)$
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

2. Draw the schematic for your circuit showing IC Chip numbers, pin numbers and power supply connections.

- 3.) What is the Boolean Logic Expression for your circuit?