

Fall 2018

Name SAMPLE

Show all work - No work, No Credit!!!!

Grade: Independence, Construction & Measurement, Communication

\_\_\_\_\_ , \_\_\_\_\_ , \_\_\_\_\_

### EET-200L Lab Practical #1

You are to build the circuit shown below. You are to perform this part of the lab on your own. If you must ask your instructor a question, you may be penalized up to 5 pts. per question if the question pertains to material you should know from the lab exercises performed this semester. You may use your previous labs for reference.

#### Procedure:

1. Construct the circuit below using the zener diode provide to you by your instructor. Use your GW Instek signal generator (not the one built into the oscilloscope or the center-tapped voltages on the PAD-234 Trainer kits.) Your instructor will give you the proper diode to use.

2. Use your oscilloscope to create a display on the oscilloscope which contains the following:

a. Displays for  $V_{in}$  and  $V_{out}$ .

b. Measurements for the maximum and minimum voltages of  $V_{in}$  and  $V_{out}$ .

c. First Initial and first three letters of your Last Name.

3. Have your instructor verify your circuit & measurements. Instructor initial & date: \_\_\_\_\_

4. Take a screen shot and save it on a USB drive provided to you by your professor.

5. Take a screen shot of this display and save it on your own USB drive.

6. Post your screen shot at [cset.stcc.edu/forums](http://cset.stcc.edu/forums) EET-200 Lab Forums under the Lab Practical #1 notification post.

7. E-mail a copy of your screen shot to your professor. ([Jagodowski@stcc.edu](mailto:Jagodowski@stcc.edu))

8. Submit this sheet to your professor.

